

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number		10001477
Filing Date		2001-11-01
First Named Inventor	Steve Roe	
Art Unit	2123	
Examiner Name	Jason Scott Proctor	
Attorney Docket Number	CD01203M	

/J.P./	19	USPTO Non-Final Rejection for Application Number 10/327,207 (CD01192) dated 09/20/2005; 11 pages	<input type="checkbox"/>
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/J.P./	26	USPTO Advisory Action for Application Number 09/989,570 (CD01167M) dated 08/14/2003; 3 pages	<input type="checkbox"/>
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/J.P./	30	USPTO Non-Final Rejection for Application Number 09/989,571 (CD01168M) dated 05/23/2005; 6 pages	<input type="checkbox"/>
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/J.P./	33	M. Morris Mano. "Computer System Architecture," 1982, Prentice-Hall, 2nd Edition, pp. 261-264 and 435-440: 14 pages	<input type="checkbox"/>
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/J.P./	35	Fred Eady, "PSOC 101," Circuit Cellar, August 2004, accessed 09/13/2005. < http://www.circuitcellar.com/library/print/0804/eady169/2.htm >; 4 pages	<input type="checkbox"/>
/J.P./	36	USPTO Notice of Allowance for Application Number 10/803,030 (CD00167C1) dated 01/08/2007; 6 pages	<input type="checkbox"/>
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/J.P./	38	Application Number 12/058,586 (CD2007122): "System and Method for Monitoring a Target Device," Ogami et al., filed on 03/28/2006; 41 pages	<input type="checkbox"/>
/J.P./	39	Application Number 12/058,534 (CD2007123): "System and Method for Controlling a Target Device," Ogami et al., filed on 03/28/2006; 40 pages	<input type="checkbox"/>
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/J.P./	41	Application Number 12/058,569 (CD2007125): "Configuration of Programmable IC Design Elements," Best et al., filed on 03/28/2008; 19 pages	<input type="checkbox"/>
/J.P./	42	Application Number 12/057,149 (CD2007156): "Power Management Architecture, Method and Configuration System," Kenneth Ogami, filed on 03/27/2008; 34 pages	<input type="checkbox"/>
/J.P./	43	Application Number 12/765,400 (CD2009119): "Autonomous Control in a Programmable System," Sullam et al., filed on 04/22/2010; 30 pages	<input type="checkbox"/>
/J.P./	44	International Search Report for International Application Number PCT/US10/33626 (CD2009119PCT) mailed 06/24/2010; 3 pages	<input type="checkbox"/>
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/J.P./	46	USPTO Non-Final Rejection for Application Number 12/060,176 (CD2007136) dated 03/30/2010; 22 pages	<input type="checkbox"/>
/J.P./	47	John Mangino, "Using DMA with High Performance Peripherals to Maximize System Performance," 2007, Texas Instruments, pp. 1-23; 23 pages	<input type="checkbox"/>
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U.S. PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
/J.P./	1	5935233	B1	1999-08-10	Jeddeloh, Joseph	Entire Document
/J.P./	2	4807183	B1	1989-02-21	Kung et al.	Entire Document
/J.P./	3	5377333	B1	1994-12-27	Nakagoshi et al.	Entire Document
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/J.P./	9	7516509	B1	2009-11-10	Qureshi et al.	Entire Document
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Attorney Docket Number	GD01203M	

/J.P./	20	5497119	B1	1998-03-05	Tedrow et al.	Entire Document
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U.S. PATENT APPLICATION PUBLICATIONS

Examiner Initial*	Cite No	Publication Number	Kind Code ¹	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	20070258458	A1	2007-11-08	Kayser, Robert	Entire Document
/J.P./	2	20050066152	A1	2005-03-24	Garey, Kenneth	Entire Document
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